

# DESIGN OF GSM BLOCKING SYSTEM (JAMMER)

MUHAMMED BAKIR  
SEYİT ALİ OCAK  
HALİL KARADEMİR

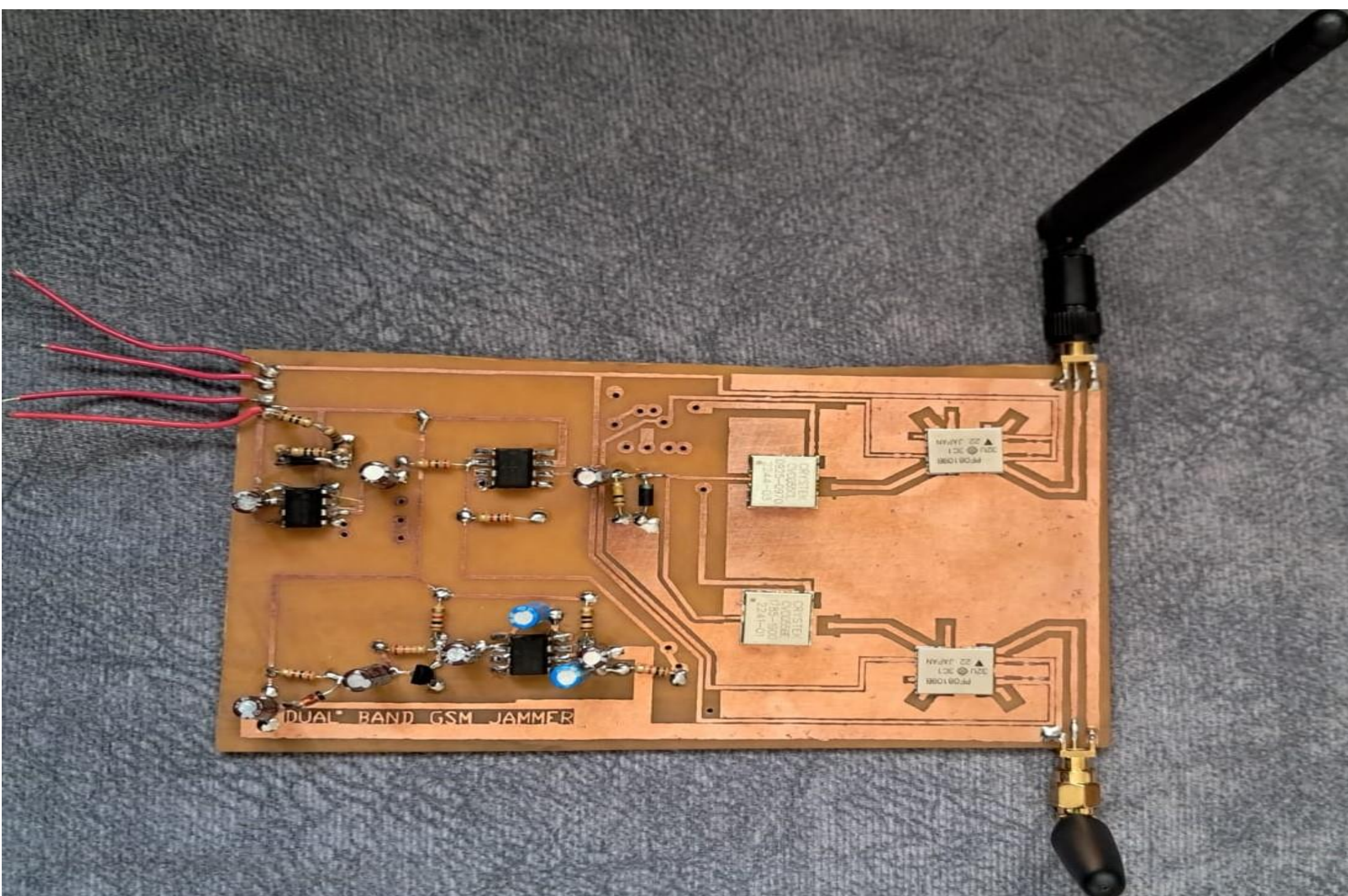


Supervisor Asst. Prof. Dr. SERKAN ÖZBAY

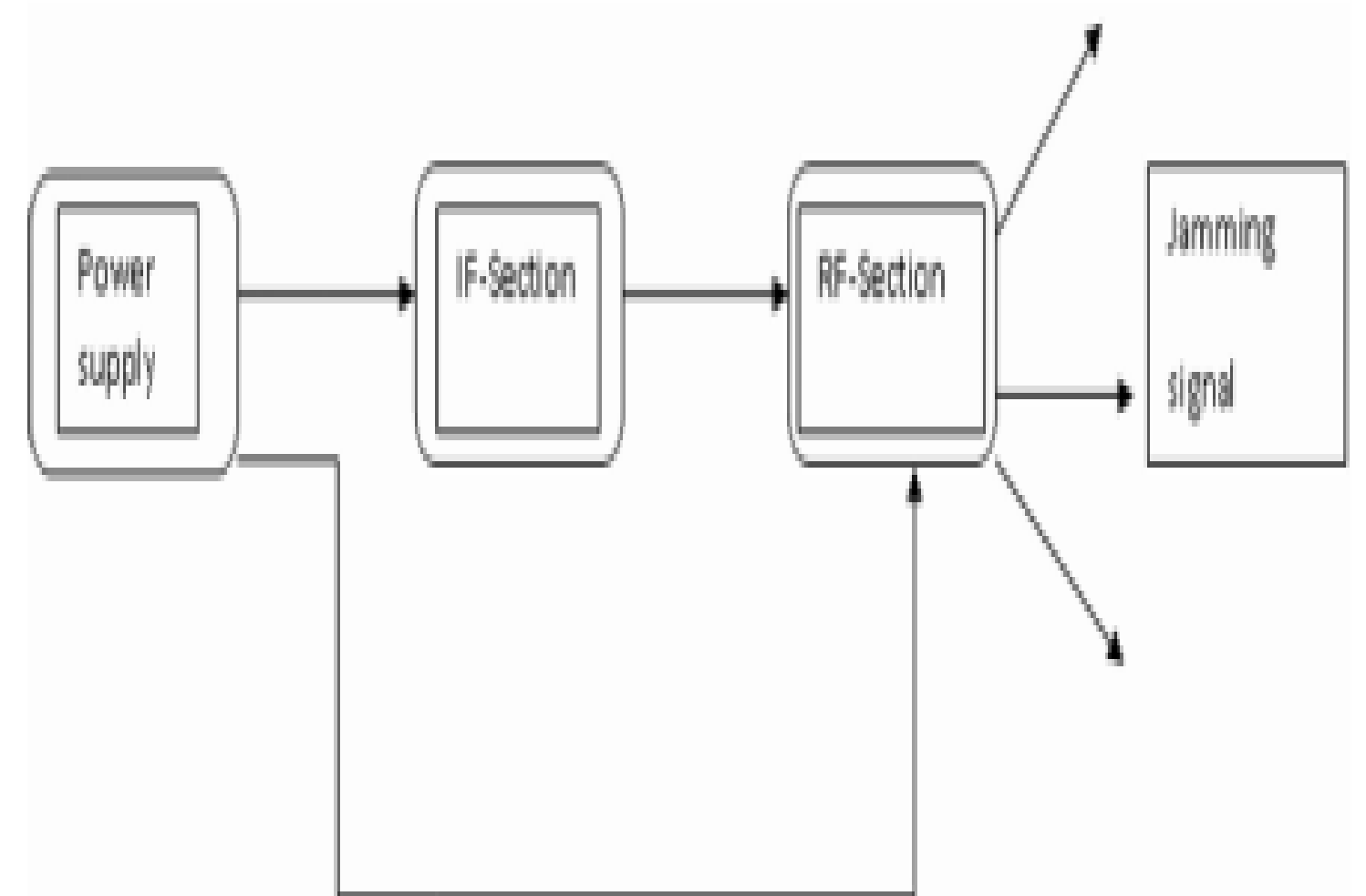
Department of Electrical and Electronics Engineering, University of Gaziantep, Turkey.

## Abstract

Phones are an integral part of our lives. Daily phone usage time in Turkey is 5.5 hours. There is no doubt that phones have benefits, but their harms should not be ignored. It is requested that there be no sound from phones in theaters, cinemas, mosque, meeting rooms, etc. One way to achieve this is to use a jammer in the desired location.



## Signal Flow Chart



## JAMMER DEVICE DESIGN

A jammer circuit device is designed and constructed with integrated chips. Jammer device consist of 2 main circuits which are named intermediate frequency section and radio frequency section. These sections produce signal ranges between 900MHZ and 1.8GHZ. These signals are combined at the antenna. Signals from the antenna suppress the connection between the base station and the phone.

## Main Components

- CVCO55BE (VCO)
- CVCO55CL (VCO)
- 555TIMER
- LM386 (Audio amplifier)
- LM741 Opamp
- Antennas
- PF08109B (power amplifier)

## CONCLUSION

As a result of our project , a jammer circuit has been constructed . This circuit produces signals and combines them to block mobile phone and base station communication. We tried to block the band intervals we determined.

## References

- <https://www.just.edu.jo/~nihad/files/mat/591/Jammer%20Final%20Report.pdf>
- <https://www.scribd.com/doc/98163011/Mobile-Jammer-Report>
- <https://www.slideshare.net/ARYANKUMAR57/project-report-on-signal-jammer>